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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group art unit: 2841  
Examiner: Yuriy Semenenko  
Conf. number: 4886

In Re PATENT APPLICATION of:

Applicant(s): Seiichiro SASAKI et al. )

Serial No.: 10/812,962 )

Filing Date: March 31, 2004 )

For: MULTILAYERED POWER SUPPLY )  
LINE FOR SEMICONDUCTOR )  
INTEGRATED CIRCUIT AND LAYOUT )  
METHOD THEREOF )

**AMENDMENT**

Atty. ref.: OKI 417 )

\_\_\_\_\_  
May 29, 2007

**Mail Stop Amendment**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Examiner's Action mailed on March 22, 2007, please  
amend the above-identified application as follows: